



Attorney's Docket No. : 10559-310US1 / P9631

PCT Receipt
DT05 Rec'd PCT/PTO 28 OCT 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al. Art Unit : Unknown
Serial No. : 10/070,091 Examiner : Unknown
Filed : February 27, 2002
Title : REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR
ARCHITECTURE

Commissioner for Patents
Washington, D.C. 20231

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REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT Technology Center 2100

Please correct the Filing Receipt for the above-referenced application to include the correct Priority data as follows:

"THIS APPLICATION IS A 371 OF PCT/US00/23993 08/31/2000
WHICH CLAIMS BENEFIT OF 60/151,961 09/01/1999"

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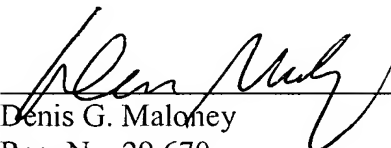
Applicant: Gilbert Wolrich et al.
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Page : 2

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Respectfully submitted,

Date: _____

10/21/02



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/070,091	06/28/2002	2183	266	10559-310US1	6	22	3

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CONFIRMATION NO. 7309
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Date Mailed: 08/06/2002

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Applicant(s)

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Priority Date of 60/151,961 should be

DOCKETED BY PRACTICE SYSTEM

Action Code Car. OFRBase Date 8-6-02Due Date 9-6-02Deadline -Initials PEC On -

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/US00/23993 08/31/2000
WHICH CLAIMS BENEFIT OF 60/151,961 02/22/2000

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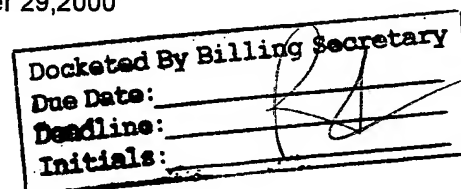
Technology Center 2100

Foreign Applications

Projected Publication Date: Not Applicable, filed prior to November 29, 2000

Non-Publication Request: No

Early Publication Request: No



Title

Register set used in multithreaded parallel processor architecture

REPORTED
TO INTEL

Preliminary Class

712

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